Last Update: September 2, 2020

Version: 1.0

Subject: Salinas v1.1 Update

Summary of Update by page #

Page 1: no change; version # only

Page 2:

1. Change resistor value from 10KOhm to 1KOhm for R17, R20, R18, R21, R19 and R22 (BOM change)

Page 3:

1. Remove R23, R24, R25, R26, R27 and R28 (BOM change)
2. Connect VCCB to 3P3V
3. Connect VCCA to VDDIO
4. Replace TXB0104DR with TXS0104EDR (BOM change)

Page 4:

1. Remove R30, R31 and R34 (BOM change)
2. Connect VCCB to 3P3V
3. Connect VCCA to VDDIO
4. Connect P5 pin 1 to 3P3V
5. Replace R35, R36 and R37 from 10KOhm to 1KOhm (BOM change)
6. Replace TXB0104DR with TXS0104EDR (BOM change)

Page 5:

1. Add ground connection for J6 pin 2 (error from previous design)

Page 6:

1. Rev 1.0 placement for J8 is not correct. The connector is placed too far in; there is no clearance for connecting cable.
2. Replace J8 with microUSB connector (optional)
3. Need to discuss if moving the foot-print versus replacing microUSB connector work load
4. U18 is DNP (BOM change)

Page 7:

1. Add P20, a 1x2 header; connection as shown in schematic

Page 8: No change

Page 9: No change